

AMENDMENTS

In the Claims:

Please cancel claims 79-149, 223-226, 229-233, 236-240, 243, 244, 246-250, 253-257, 260, 261, 263-267, 270-274, 277, 278, 280-284 and 287-291, and amend claims 7, 12, 13, 28, 45-78, 150, 188, 189, 191-195, 198-202, 205, 206, 208-121 and 215-219. Pursuant to the revised format for amendments, the status of all of the claims is set forth below.

Claims 1-6 (Cancelled)

7. (Currently Amended) A memory address space extension device comprising:

a definition table for defining an access right of a task with respect to a data area;

a definition table forming means for forming said definition table; [[and]]

an access right judgement means for judging, according to said definition table, whether or not the access right to the data area requested by the task should be provided with respect to the task, and said access right judgement means judges whether or not said task is permitted access according to an interruption processing number; and

an address operational means for converting a CPU address into an extended address, and a memory having the extended address converted previously, said address operational means causing said CPU address to be converted into the extended address when said task is permitted access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

8. (Previously Presented) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not said task possesses an access right for accessing a prescribed data area for any task ID associated with the task.

9. (Previously Presented) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not said task possesses an access right for accessing a prescribed data area for any task ID associated with the task and / or in every interruption processing number.

10. (Previously Presented) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether it causes the access right to the data area requested by said task to be given according to a task ID, and a data area ID indicating the data area requested by said task.

11. (Previously Presented) A memory address space extension device as claimed in claim 8, wherein said access right judgement means judges whether to permit access to the data area requested by said task according to said task ID, and a data area ID indicating the data area requested by said task.

12. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether to permit access to the data area requested by said task according to said interruption processing number, and according to [[the]] a data area ID indicating the data area requested by said task.

13. (Currently Amended) A memory address space extension device as claimed in claim 9, wherein said access right judgement means judges whether to permit access to the data area requested by said task according said interruption processing number, and according to [[the]] a data area ID indicating the data area requested by said task.

14. (Previously Presented) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not access is permitted with respect to a prescribed data area in every said task.

15. (Previously Presented) A memory address space extension device as claimed in claim 8, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

16. (Previously Presented) A memory address space extension device as claimed in claim 9, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

17. (Previously Presented) A memory address space extension device as claimed in claim 10, wherein said definition table defines whether or not access is permitted with respect to a prescribed data area in every said task.

18. (Previously Presented) A memory address space extension device as claimed in claim 11, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

19. (Previously Presented) A memory address space extension device as claimed in claim 12, wherein said definition table defines whether or not access is permitted with respect to a prescribed data area in every said task.

20. (Previously Presented) A memory address space extension device as claimed in claim 13, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

21. (Previously Presented) A memory address space extension device as claimed in claim 7, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to a prescribed data area in every said task.

22. (Previously Presented) A memory address space extension device as claimed in claim 8, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

23. (Previously Presented) A memory address space extension device as claimed in claim 9, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;

(3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

24. (Previously Presented) A memory address space extension device as claimed in claim 10, wherein said definition table defines any one of:

(1) causing only writing of the data to be permitted;

(2) causing reading of the data to be permitted;

(3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to a prescribed data area in every said task.

25. (Previously Presented) A memory address space extension device as claimed in claim 11, wherein said definition table defines any one of:

(1) causing only writing of the data to be permitted;

(2) causing reading of the data to be permitted;

(3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

26. (Previously Presented) A memory address space extension device as claimed in claim 12, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to a prescribed data area in every said task.

27. (Previously Presented) A memory address space extension device as claimed in claim 13, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

28. (Currently Amended) A memory address space extension device as claimed in claim 9, further comprising an interruption number generation means for generating the

[[the]] interruption processing number with respect to interruption processing according to hardware.

29. (Previously Presented) A memory address space extension device as claimed in claim 12, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

30. (Previously Presented) A memory address space extension device as claimed in claim 13, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

31. (Previously Presented) A memory address space extension device as claimed in claim 14, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

32. (Previously Presented) A memory address space extension device as claimed in claim 15, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

33. (Previously Presented) A memory address space extension device as claimed in claim 16, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

34. (Previously Presented) A memory address space extension device as claimed in claim 17, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

35. (Previously Presented) A memory address space extension device as claimed in claim 18, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

36. (Previously Presented) A memory address space extension device as claimed in claim 19, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

37. (Previously Presented) A memory address space extension device as claimed in claim 20, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

38. (Previously Presented) A memory address space extension device as claimed in claim 21, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

39. (Previously Presented) A memory address space extension device as claimed in claim 22, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

40. (Previously Presented) A memory address space extension device as claimed in claim 23, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

41. (Previously Presented) A memory address space extension device as claimed in claim 24, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

42. (Previously Presented) A memory address space extension device as claimed in claim 25, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

43. (Previously Presented) A memory address space extension device as claimed in claim 26, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

44. (Previously Presented) A memory address space extension device as claimed in claim 27, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

45. (Currently Amended) A memory address space extension device as claimed in claim 9, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

46. (Currently Amended) A memory address space extension device as claimed in claim 12, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

47. (Currently Amended) A memory address space extension device as claimed in claim 13, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

48. (Currently Amended) A memory address space extension device as claimed in claim 14, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

49. (Currently Amended) A memory address space extension device as claimed in claim 15, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

50. (Currently Amended) A memory address space extension device as claimed in claim 16, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

51. (Currently Amended) A memory address space extension device as claimed in claim 17, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

52. (Currently Amended) A memory address space extension device as claimed in claim 18, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

53. (Currently Amended) A memory address space extension device as claimed in claim 19, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

54. (Currently Amended) A memory address space extension device as claimed in claim 20, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

55. (Currently Amended) A memory address space extension device as claimed in claim 21, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

56. (Currently Amended) A memory address space extension device as claimed in claim 22, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

57. (Currently Amended) A memory address space extension device as claimed in claim 23, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

58. (Currently Amended) A memory address space extension device as claimed in claim 24, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

59. (Currently Amended) A memory address space extension device as claimed in claim 25, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

60. (Currently Amended) A memory address space extension device as claimed in claim 26, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

61. (Currently Amended) A memory address space extension device as claimed in claim 27, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

62. (Currently Amended) A memory address space extension device as claimed in claim 28, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

63. (Currently Amended) A memory address space extension device as claimed in claim 29, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

64. (Currently Amended) A memory address space extension device as claimed in claim 30, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

65. (Currently Amended) A memory address space extension device as claimed in claim 31, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

66. (Currently Amended) A memory address space extension device as claimed in claim 32, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

67. (Currently Amended) A memory address space extension device as claimed in claim 33, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

68. (Currently Amended) A memory address space extension device as claimed in claim 34, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

69. (Currently Amended) A memory address space extension device as claimed in claim 35, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

70. (Currently Amended) A memory address space extension device as claimed in claim 36, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

71. (Currently Amended) A memory address space extension device as claimed in claim 37, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

72. (Currently Amended) A memory address space extension device as claimed in claim 38, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

73. (Currently Amended) A memory address space extension device as claimed in claim 39, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

74. (Currently Amended) A memory address space extension device as claimed in claim 40, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

75. (Currently Amended) A memory address space extension device as claimed in claim 41, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

76. (Currently Amended) A memory address space extension device as claimed in claim 42, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

77. (Currently Amended) A memory address space extension device as claimed in claim 43, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

78. (Previously Presented) A memory address space extension device as claimed in claim 44, further comprising an interruption number decoding means for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

79. (Previously Presented) A memory address space extension device as claimed in claim 7, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

Claims 79-149 (Cancelled)

150. (Currently Amended) A storage medium storing therein a program in order to execute the processing of:

a definition table preparation processing for preparing a definition table;

a reference processing for said definition table which defines an access right of a task with respect to a data area; and

an access right judgement processing for judging whether or not said task is permitted access for the data area requested by said task according to said definition table, and said access right judgement processing judges whether or not said task is permitted access according to an interruption processing number,

wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted access to said data area according to said access right judgement processing, before executing supporting processing by a memory with the extended address.

151. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing a prescribed data area to be seen for any task ID associated with said task.

152. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes the definition table defining whether or not said task possesses the access right for accessing a prescribed data area to be seen for any task ID given to said task and / or in every interruption number.

153. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said access right judgement processing judges whether or not said task possesses the access right for accessing a prescribed data area according to a task ID and / or a data area ID indicating the data area requested by said task.

154. (Previously Presented) A storage medium storing therein a program as claimed in claim 151, wherein said access right judgement processing judges whether or not said task possesses the access right for accessing said prescribed data area according to said task ID and / or a data area ID indicating the data area requested by said task.

155. (Cancelled)

156. (Cancelled)

157. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

158. (Previously Presented) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing said prescribed data area to be seen in every said task.

159. (Previously Presented) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing said prescribed data area to be seen in every said task.

160. (Previously Presented) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing said prescribed data area to be seen in every said task.

161. (Previously Presented) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing said prescribed data area to be seen in every said task.

162. (Cancelled)

163. (Cancelled)

164. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes any one of:

(1) allowing only writing of the data to be permitted;

(2) allowing reading of the data to be permitted;

(3) allowing writing of the data and reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to a prescribed data area in every said task.

165. (Previously Presented) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table causes any one of:

(1) allowing only writing of the data to be permitted;

- (2) allowing reading of the data to be permitted;
 - (3) allowing writing of the data and reading of the data to be permitted; and
 - (4) allowing no-access to be permitted,
- to be seen with respect to said prescribed data area in every said task.

166. (Previously Presented) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table causes any one of:

- (1) allowing only writing of the data to be permitted;
 - (2) allowing reading of the data to be permitted;
 - (3) allowing writing of the data and reading of the data to be permitted; and
 - (4) allowing no-access to be permitted,
- to be seen with respect to said prescribed data area in every said task.

167. (Previously Presented) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table causes any one of:

- (1) allowing only writing of the data to be permitted;

- (2) allowing reading of the data to be permitted;
 - (3) allowing writing of the data and reading of the data to be permitted; and
 - (4) allowing no-access to be permitted,
- to be seen with respect to said prescribed data area in every said task.

168. (Previously Presented) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table causes any one of:

- (1) allowing only writing of the data to be permitted;
 - (2) allowing reading of the data to be permitted;
 - (3) allowing writing of the data and reading of the data to be permitted; and
 - (4) allowing no-access to be permitted,
- to be seen with respect to said prescribed data area in every said task.

169. (Cancelled)

170. (Cancelled)

171. (Previously Presented) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number generation

processing for generating the interruption processing number to interruption processing according to hardware.

172. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

173. (Cancelled)

174. (Previously Presented) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

175. (Previously Presented) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

176. (Previously Presented) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

177. (Previously Presented) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

178. (Previously Presented) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

179. (Cancelled)

180. (Cancelled)

181. (Previously Presented) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

182. (Previously Presented) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

183. (Previously Presented) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

184. (Previously Presented) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

185. (Previously Presented) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

186. (Cancelled)

187. (Cancelled)

188. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

189. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

190. (Cancelled)

191. (Currently Amended) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

192. (Currently Amended) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number decoding

processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

193. (Currently Amended) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

194. (Currently Amended) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

195. (Currently Amended) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

196. (Cancelled)

197. (Cancelled)

198. (Currently Amended) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

199. (Currently Amended) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number decoding

processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

200. (Currently Amended) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

201. (Currently Amended) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

202. (Currently Amended) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

203. (Cancelled)

204. (Cancelled)

205. (Currently Amended) A storage medium storing therein a program as claimed in claim 171, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

206. (Currently Amended) A storage medium storing therein a program as claimed in claim 172, wherein said program further executes interruption number decoding

processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

207. (Cancelled)

208. (Currently Amended) A storage medium storing therein a program as claimed in claim 174, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

209. (Currently Amended) A storage medium storing therein a program as claimed in claim 175, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

210. (Currently Amended) A storage medium storing therein a program as claimed in claim 176, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

211. (Currently Amended) A storage medium storing therein a program as claimed in claim 177, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

212. (Currently Amended) A storage medium storing therein a program as claimed in claim 178, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

213. (Cancelled)

214. (Cancelled)

215. (Currently Amended) A storage medium storing therein a program as claimed in claim 181, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

216. (Currently Amended) A storage medium storing therein a program as claimed in claim 182, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

217. (Currently Amended) A storage medium storing therein a program as claimed in claim 183, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

218. (Currently Amended) A storage medium storing therein a program as claimed in claim 184, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

219. (Currently Amended) A storage medium storing therein a program as claimed in claim 185, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction ~~with respect to said interruption instruction~~ according to software.

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(PATENT)

220. (Cancelled)

221. (Cancelled)

Claims 222-293 (Cancelled)